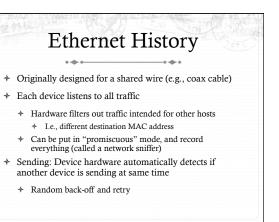
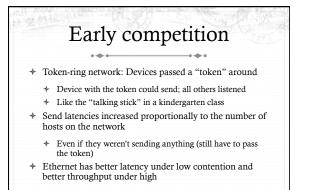
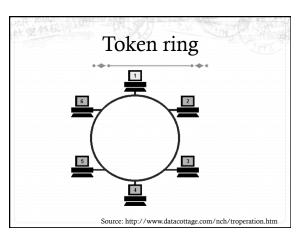


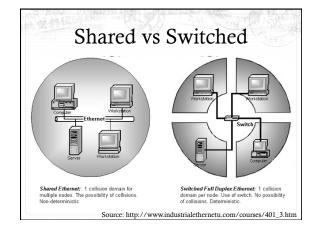
Ethernet (or 802.2 or 802.3) * All slight variations on a theme (3 different standards)

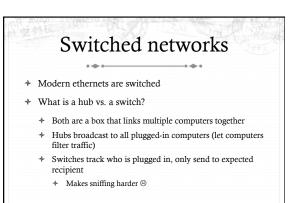
- ✤ Simple packet layout:
 - Header: Type, source MAC address, destination MAC address, length, (and a few other fields)
 - Data block (payload)
 - * Checksum
- $* \ \ Higher-level \ protocols \ ``nested" \ inside \ payload$
- + "Unreliable" no guarantee a packet will be delivered











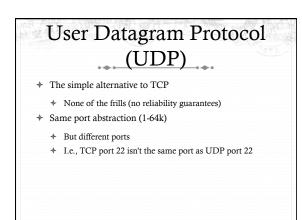
Internet Protocol (IP)

2 flavors: Version 4 and 6

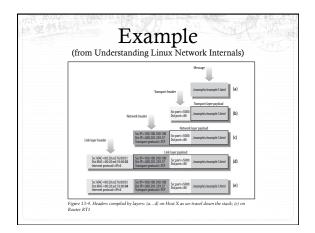
- Version 4 widely used in practice---today's focus
- Provides a network-wide unique device address (IP address)
- This layer is responsible for routing data across multiple ethernet networks on the internet
 - * Ethernet packet specifies its payload is IP
 - At each router, payload is copied into a new point-to-point ethernet frame and sent along

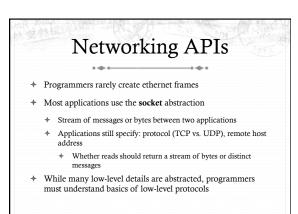
Transmission Control Protocol (TCP)

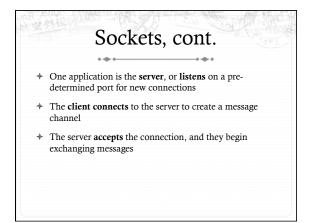
- Higher-level protocol that layers end-to-end reliability, transparent to applications
 - Lots of packet acknowledgement messages, sequence numbers, automatic retry, etc.
 - Pretty complicated
- Applications on a host are assigned a *port* number
 - + A simple integer from 0-64k
 - * Multiplexes many applications on one device
 - + Ports below 1k reserved for privileged applications

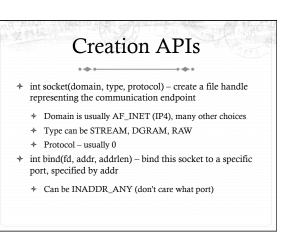


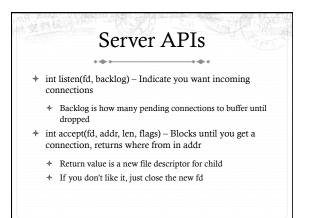
Some well-known ports * 80 - http * 22 - ssh * 53 - DNS * 25 - SMTP

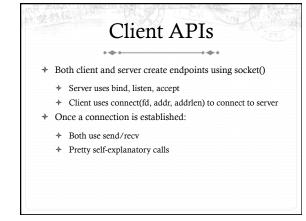




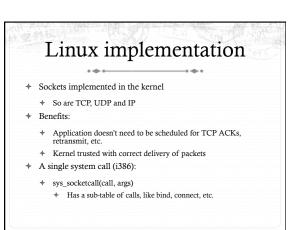


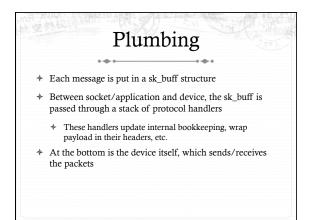


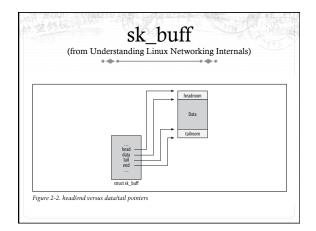


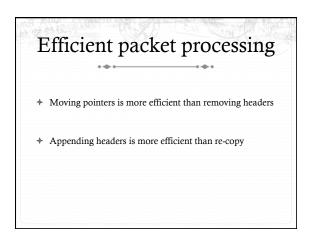


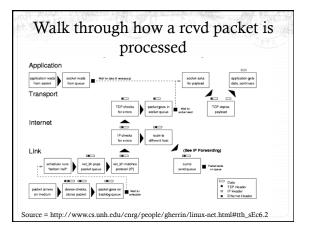




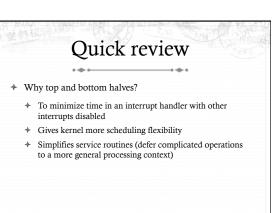






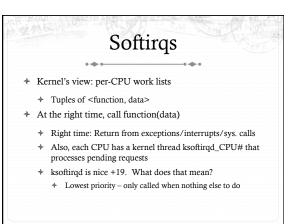


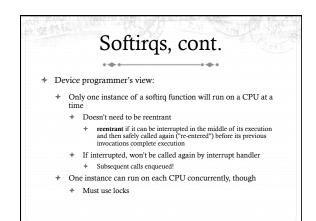
* "Top half" responsible to: * Allocate a buffer (sk_buff) * Copy received data into the buffer * Initialize a few fields * Call "bottom half" handler * In some cases, sk_buff can be pre-allocated, and network card can copy data in (DMA) before firing the interrupt * Lab 6 will follow this design

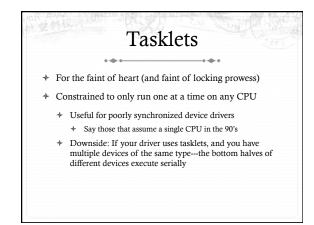


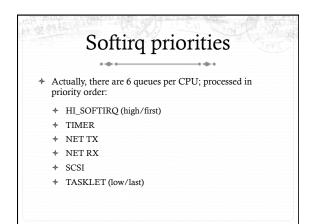
Digression: Softirqs

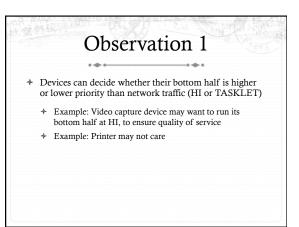
- + A hardware IRQ is the hardware interrupt line
 - Also used for hardware "top half"
- Soft IRQ is the associated software "interrupt" handler
 Or, "bottom half"
- + How are these implemented in Linux?
 - * Two canonical ways: Softirq and Tasklet
 - More general than just networking

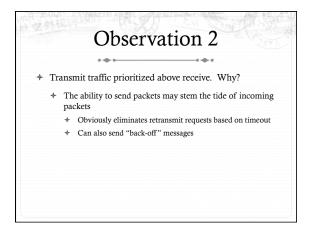


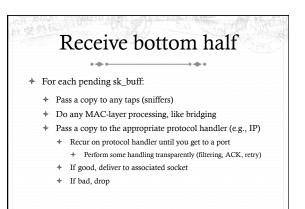










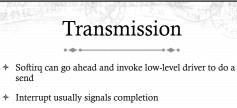


Socket delivery

- + Once the bottom half/protocol handler moves a payload into a socket:
- Check and see if the task is blocked on input for this socket
- If so, wake it up
- ✤ Read/recv system calls copy data into application

Socket sending

- Send/write system calls copy data into socket
 - Allocate sk_buff for data
- * Be sure to leave plenty of head and tail room!
- System call does protocol handling during application's timeslice
- * Note that receive handling done during ksoftirqd timeslice
- + Last protocol handler enqueues a softirq to transmit



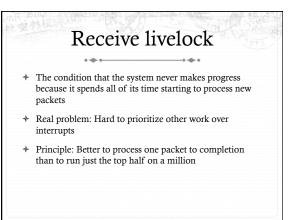
* Interrupt handler just frees the sk_buff

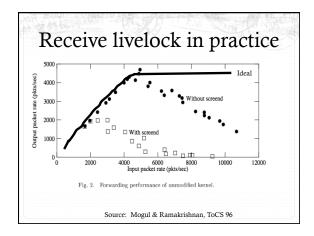
Switching gears

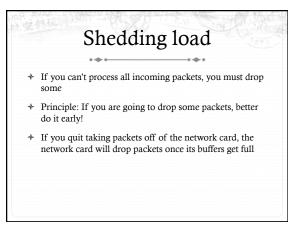
- We've seen the path network data takes through the kernel in some detail
- Now, let's talk about how network drivers handle heavy loads

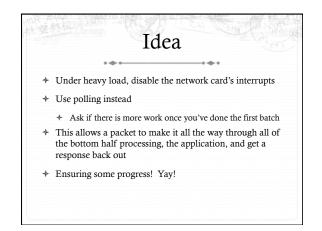
Our cup runneth over

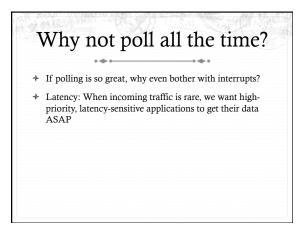
- ✤ Suppose an interrupt fires every time a packet comes in
 - + This takes N ms to process the interrupt
- What happens when packets arrive at a frequency approaching or exceeding N?
 - * You spend all of your time handling interrupts!
- Will the bottom halves for any of these packets get executed?
 - * No. They are lower-priority than new packets

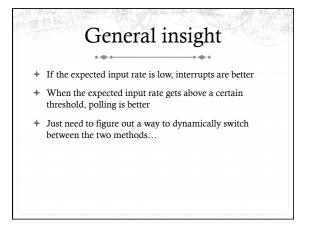


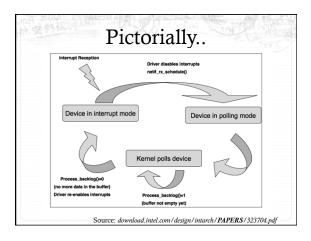






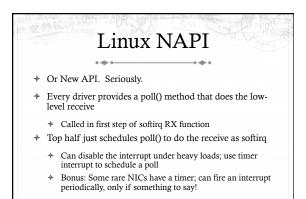


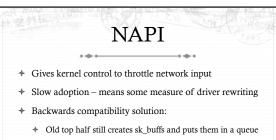




Why haven't we seen this before? + Why don't disks have this problem? + Inherently rate limited + If the CPU is bogged down processing previous disk

requests, it can't issue more
An external CPU can generate all sorts of network inputs





- Queue assigned to a fake "backlog" device
- * Backlog poll device is scheduled by NAPI softirq
- Interrupts can still be disabled

NAPI Summary

- * Too much input is a real problem
- NAPI lets kernel throttle interrupts until current packets processed
- + Softirq priorities let some devices run their bottom halves before net TX/RX
 - * Net TX handled before RX

General summary Networking basics and APIs Idea of plumbing from socket to driver Through protocol handlers and softirq poll methods NAPI and input throttling