

Notes 2

Arguments

- premises (assumptions)

Testing an argument form for validity

- truth table

- critical row

Rules of inference (valid arguments forms)

- Table 2.3.1 (to understand instead of to memorize, just like Theorem 2.1.1 in Sec. 2.1)

Digital logic circuits

- inputs – variables

- circuits – gates

- input/output table – truth table

Black box circuits

- design circuits from input/output table – derive logic expressions from truth table

- disjunctive normal form (DNF)

- conjunctive normal form (CNF) (not in the textbook)

NAND gate, NOR gate

- functionally complete (it is what Example 2.4.7 means but not formally in the textbook)